

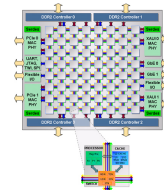
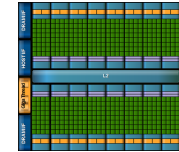
Architecting Parallel Software with Patterns

Kurt Keutzer, EECS, Berkeley

with thanks to Tim Mattson, Intel
 and the PALLAS team:

The Challenge of Parallelism

Programming parallel processors is one of the challenges of our era



NVIDIA Tegra 2 system on a chip (SoC)

- Dual-core ARM Cortex A9.
- Integrated GPU. Lots of DSP.
- 1 GHz.
- 2 single-precision GFLOPs peak (CPUs only)

Nvidia Fermi

- 16 cores, 48-way multithreaded,
- 4-wide Superscalar, dual-issue, 3
- 2-wide SIMD (half-pumped)
- 2 MB (16 x 128 KB) Registers, 1
- MB (16 x 64 KB) L1 cache, 0.75 MB L2 Cache

Tiler Tile64

- 64 processors
- Each tile has L1, L2, can run OS
- 443 billion operations/sec.
- 500-833 MHz
- 50 Gbytes/sec memory bandwidth

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2

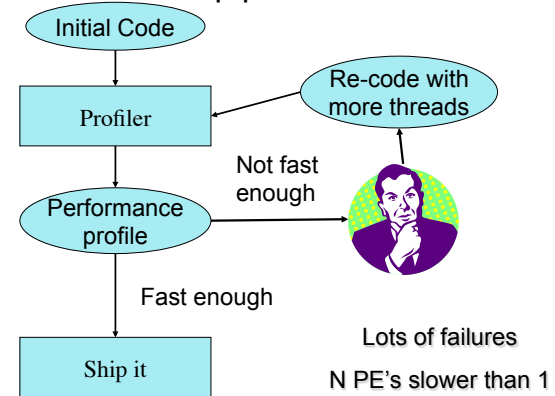
Outline

- ➔ ■ What doesn't work
- Pieces of the problem ... and solution
- General approach to architecting parallel sw
- Detail on Structural Patterns
- Detail on Computational Patterns
- High-level examples of architecting applications

3

Assumption #1:

How NOT to develop parallel code



4

4

Steiner Tree Construction Time By Routing Each Net in Parallel

Benchmark	Serial	2 Threads	3 Threads	4 Threads	5 Threads	6 Threads
adaptec1	1.68	1.68	1.70	1.69	1.69	1.69
newblue1	1.80	1.80	1.81	1.81	1.81	1.82
newblue2	2.60	2.60	2.62	2.62	2.62	2.61
adaptec2	1.87	1.86	1.87	1.88	1.88	1.88
adaptec3	3.32	3.33	3.34	3.34	3.34	3.34
adaptec4	3.20	3.20	3.21	3.21	3.21	3.21
adaptec5	4.91	4.90	4.92	4.92	4.92	4.92
newblue3	2.54	2.55	2.55	2.55	2.55	2.55
average	1.00	1.0011	1.0044	1.0049	1.0046	1.0046

5

Hint: What is this person thinking of?

Re-code with more threads

Edward Lee, "The Problem with Threads"

Threads, locks, semaphores, data races

6

So What's the Alternative?

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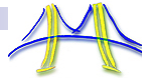
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Principles of SW Design

After 15 years in industry, at one time overseeing the technology of 25 software products, my best principle to facilitate good software design is modularity:

Modularity helps:

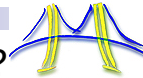
- Architect: Makes overall design sound and comprehensible
- Project manager:
 - As a manager I am able to comfortably assign different modules to different developers
 - I am also able to use module definitions to track development
 - Build a PERT chart for development progress
 - Build a "control panel" for current software quality
- Module implementors: As a module implementor I am able to focus on the implementation, optimization, and verification of my module with a minimum of concern about the rest of the design
- Modularity helps to identify key computations



What's life like without modularity?

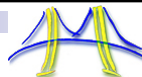
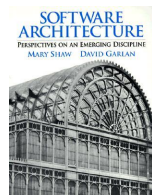
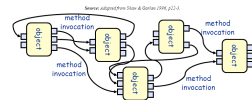
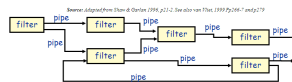
- Spaghetti code
- Wars over the interpretation of the specification
- Waiting on other coders
- Wondering why you didn't touch anything and now your code broke
- Hard to verify your code in isolation, and therefore hard to optimize
- Hard to parallelize without identifying key computations

- Modularity will help us obviate all these
 - Parnas, "On the criteria to be used on composing systems into modules," CACM, December 1972.



Big Step: Architectural Styles (Garland and Shaw, 1996)

- Pipe and filter
- Object oriented
- Event based
- Layered
- Agent and repository
- Process control



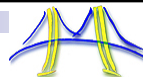
Object-Oriented Programming

Focused on:

- Program modularity
- Data locality
- Architectural styles
- Design patterns


Neglected:

- Application concurrency
- Computational details
- Parallel implementations



What's missing?: Is an executing software program more like?

a) A building b) A factory



We need to consider the machinery – but what is the machinery?

Computations are the Machinery

HPC knows a lot about computations, application concurrency, efficient programming, and parallel implementation

$$x_c \leftarrow \sum_j g_{cj} * x_j$$

$$x \leftarrow WS_\lambda \{W^* x\}$$

$$x \leftarrow F(P^T y + P_c^T P_c F^* x)$$

$$\frac{\partial V}{\partial t} + \frac{1}{2} \sigma^2 S^2 \frac{\partial^2 V}{\partial S^2} + rS \frac{\partial V}{\partial S} - rV = 0.$$

minimize $\|Wx\|_1$
s.t $F_\Omega x = y,$
 $\|Gx - x\|_2 < \varepsilon$


$$J(w) = \int_\Omega \psi_1(|I(x+w) - I(x)|^2) dx +$$

$$\gamma \int_\Omega \psi_2(|\nabla I(x+w) - \nabla I(x)|^2) dx +$$

$$\alpha \int_\Omega \psi_3(|\nabla u|^2 + |\nabla v|^2) dx$$

14


COMPUTATIONAL RESEARCH DIVISION



Defining Software Requirements for Scientific Computing

Phillip Colella
Applied Numerical Algorithms Group
Lawrence Berkeley National Laboratory

COMPUTATIONAL RESEARCH DIVISION



High-end simulation in the physical sciences consists of seven algorithms:

- Structured Grids (including locally structured grids, e.g. AMR)
- Unstructured Grids
- Fast Fourier Transform
- Dense Linear Algebra
- Sparse Linear Algebra
- Particles
- Monte Carlo

Well-defined targets from algorithmic and software standpoint.

Remainder of this talk will consider one of them (structured grids) in detail.

Par Lab's contribution: from 7 to 13 families of computations

Apps	Embed	SPEC	DB	Games	ML	HPC	CAD	Health	Image	Speech	Music	Browser
Graph Algorithms												
Graphical Models												
Backtrack / B&B												
Finite State Mach.												
Circuits												
Dynamic Prog.												
Unstructured Grid												
Structured Grid												
Dense Matrix												
Sparse Matrix												
Spectral (FFT)												
Monte Carlo												
N-Body												

Unfortunately ... HPC approach to software architecture
Technically this is known as a *monolithic* architecture

18

How can we integrate these insights?

- We wish to find an approach to building software that gives equal support for two key problems of software design – how to structure the software and how to efficiently implement the computations

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20

Alexander's Pattern Language

Christopher Alexander's approach to (civil) architecture:

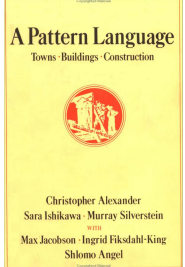

- "Each **pattern** describes a problem which occurs over and over again in our environment, and then describes the core of the solution to that problem, in such a way that you can use this solution a million times over, without ever doing it the same way twice." Page x, *A Pattern Language*, Christopher Alexander

Alexander's 253 (civil) architectural **patterns** range from the creation of cities (2. distribution of towns) to particular building problems (232. roof cap)

A **pattern language** is an organized way of tackling an architectural problem using patterns

Main limitation:

- It's about civil not software architecture!!!

21

Uses of Patterns

Patterns give names and definitions to key elements of design

This enables us to better:

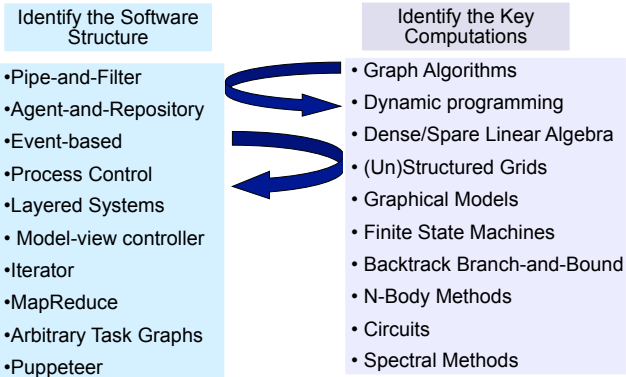
- Teach design – a palette of defined design principals
 - Gives ideas to new programmers – approaches you may not have considered
 - Gives a set of finiteness to experienced programmers – if you've considered all the patterns then you can rest assured you've considered the key approaches
- Guide design – articulate design decisions succinctly
- Communicate design – improve documentation, facilitate maintenance of software

Uses of Patterns

Patterns capture and preserve bodies of knowledge about key design decisions

- Useful implementation techniques
- Likely challenges/bottlenecks that will come with the use of this pattern (e.g. repository bottleneck in agent and repository)

Architecting Parallel Software with Patterns



Identify the Software Structure	Identify the Key Computations
•Pipe-and-Filter	• Graph Algorithms
•Agent-and-Repository	• Dynamic programming
•Event-based	• Dense/Sparse Linear Algebra
•Process Control	• (Un)Structured Grids
•Layered Systems	• Graphical Models
• Model-view controller	• Finite State Machines
•Iterator	• Backtrack Branch-and-Bound
•MapReduce	• N-Body Methods
•Arbitrary Task Graphs	• Circuits
•Puppeteer	• Spectral Methods

24

Architecting Parallel Software

Decompose Tasks

- Group tasks
- Order Tasks

Decompose Data

- Identify data sharing
- Identify data access

Identify the Software Structure

↔

Identify the Key Computations

25

Identify the SW Structure

Structural Patterns

- Pipe-and-Filter
- Agent-and-Repository
- Event-based coordination
- Iterator
- MapReduce
- Process Control
- Layered Systems

These define the structure of our software but they *do not* describe what is computed

26

Analogy: Layout of Factory Plant

27

Identify key computations

Apps	Embed	SPEC	DB	Games	ML	HPC	CAD	Health	Image	Speech	Music	Browser
Dwarves												
Graph Algorithms												
Graphical Models												
Backtrack / B&B												
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Structured Grid												
Dense Matrix												
Sparse Matrix												
Spectral (FFT)												
Monte Carlo												
N-Body												

Computational patterns describe the key computations but not how they are implemented

28

Analogy: Machinery of the Factory

29

Analogy: Architected Factory

Raises appropriate issues like scheduling, latency, throughput, workflow, resource management, capacity etc.

30

Architecting Parallel Software

Structural Patterns

- Pipe-and-Filter
- Agent-and-Repository
- Event-based
- Layered Systems
- Model-view-controller
- Arbitrary Task Graphs
- Puppeteer
- Iterator/BSP
- MapReduce

Computational Patterns

- Graph-Algorithms
- Dynamic-Programming
- Dense-Linear-Algebra
- Sparse-Linear-Algebra
- Unstructured-Grids
- Structured-Grids
- Graphical-Models
- Finite-State-Machines
- Backtrack-Branch-and-Bound
- N-Body-Methods
- Circuits
- Spectral-Methods
- Monte-Carlo

31

Remember this Poor Guy ...

Re-code with more threads

Threads, locks, semaphores, data races

Edward Lee, "The Problem with Threads"

32

What's this person thinking of ...?

- Need to integrate the insights into computation provided by HPC with the insights into program structure provided by software architectural styles

computational patterns

structural patterns

33

Outline

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34

Inventory of Structural Patterns

- pipe and filter
- iterator
- MapReduce
- blackboard/agent and repository
- process control
- Model View Controller
- layered
- event-based coordination
- puppeteer
- static task graph

35

Elements of a structural pattern

- Components are where the computation happens
- Connectors are where the communication happens
- A configuration is a graph of components (vertices) and connectors (edges)
- A structural patterns may be described as a family of graphs.

36

Pattern 1: Pipe and Filter

- Filters embody computation
- Only see inputs and produce outputs

•Pipes embody communication

May have feedback

Examples?

37

Examples of pipe and filter

- Almost every large software program has a pipe and filter structure at the highest level

Compiler

Image Retrieval System

Logic optimizer

38

Pattern 2: Iterator Pattern

Initialization condition

Variety of functions performed asynchronously

Synchronize results of iteration

iterate

Exit condition met?

Yes

No

Examples?

39

Example of Iterator Pattern: Training a Classifier: SVM Training

Update surface

Identify Outlier

iterate

All points within acceptable error?

Yes

No

Iterator Structural Pattern

40

Pattern 3: MapReduce

To us, it means

- A map stage, where data is mapped onto independent computations
- A reduce stage, where the results of the map stage are summarized (i.e. reduced)

Examples?

41

Examples of Map Reduce

- General structure:
 - Map a computation across distributed data sets
 - Reduce the results to find the best/(worst), maxima/(minima)

Support-vector machines (ML)

- Map to evaluate distance from the frontier
- Reduce to find the greatest outlier from the frontier

Speech recognition

- Map HMM computation to evaluate word match
- Reduce to find the most-likely word sequences

42

Pattern 4: Agent and Repository

Examples?

Agent and repository : Blackboard structural pattern

Agents cooperate on a shared medium to produce a result

Key elements:

- **Blackboard**: repository of the resulting creation that is shared by all agents (circuit database)
- **Agents**: intelligent agents that will act on blackboard (optimizations)
- **Manager**: orchestrates agents access to the blackboard and creation of the aggregate results (scheduler)

43

Example: Compiler Optimization

Optimization of a software program

- Intermediate representation of program is stored in the repository
- Individual agents have heuristics to optimize the program
- Manager orchestrates the access of the optimization agents to the program in the repository
- Resulting program is left in the repository

44

Example: Logic Optimization

- Optimization of integrated circuits
- Integrated circuit is stored in the repository
- Individual agents have heuristics to optimize the circuitry of an integrated circuit
- Manager orchestrates the access of the optimization agents to the circuit repository
- Resulting optimized circuit is left in the repository

45

Pattern 5: Process Control

Source: Adapted from Shaw & Garlan 1996, p27-31.

- Process control:
 - **Process:** underlying phenomena to be controlled/computed
 - **Actuator:** task(s) affecting the process
 - **Sensor:** task(s) which analyze the state of the process
 - **Controller:** task which determines what actuators should be effected

Examples?

46

Examples of Process Control

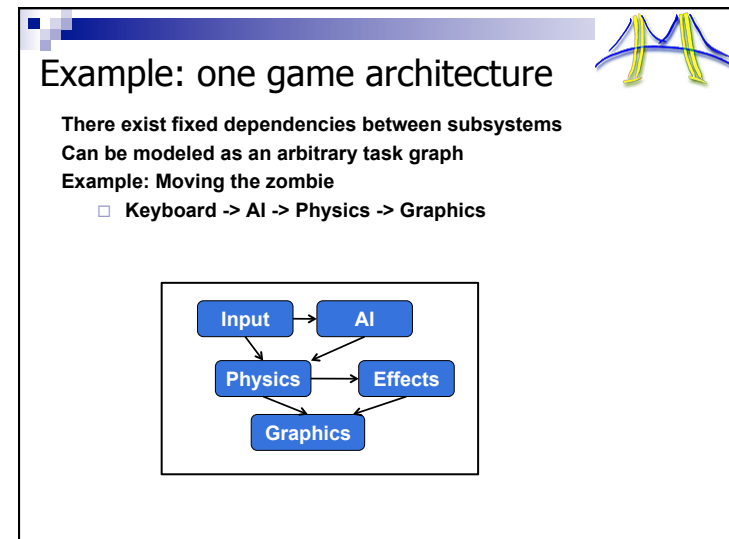
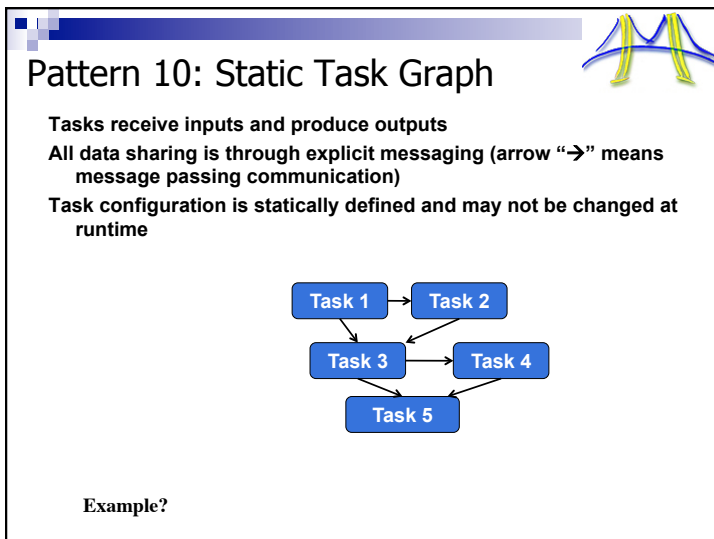
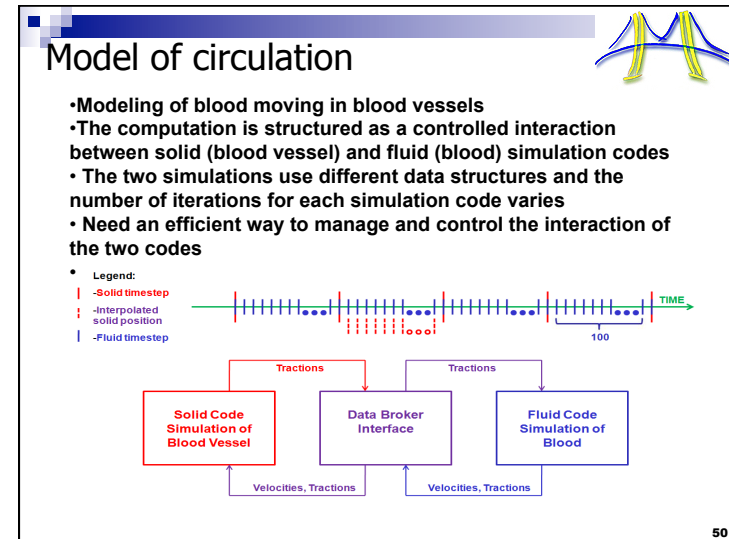
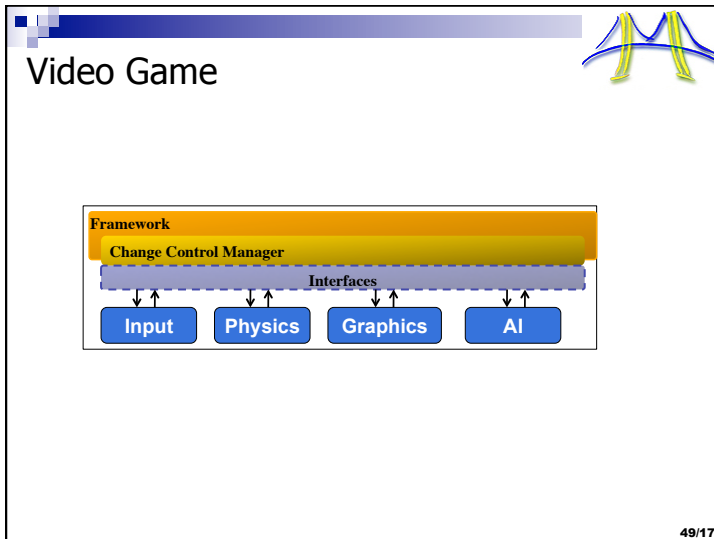
47

Pattern 9: Puppeteer

- Need an efficient way to manage and control the interaction of multiple simulators/computational agents
- Puppeteer Pattern – guides the interaction between the tasks/puppets to guarantee correctness of the overall task
- Puppeteer: 1) schedules puppets 2) manages exchange of data between puppets
- Difference with agent and repository?
 - No central repository
 - Data transfer between tasks/puppets

Examples?

48/17



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53

You explore these every class

Apps	Embed	SPEC	DB	Games	ML	HPC	CAD	Health	Image	Speech	Music	Browser
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54

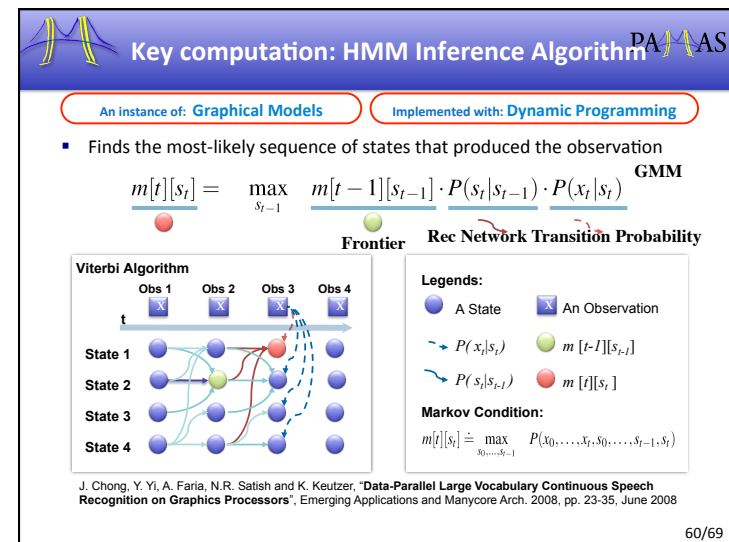
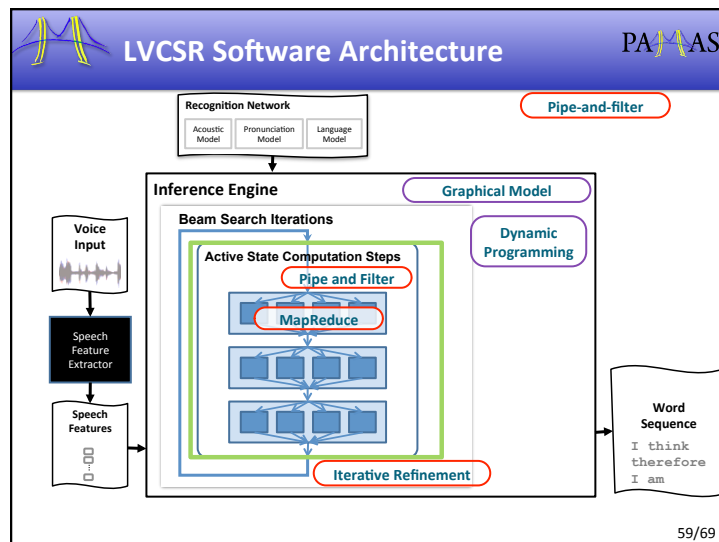
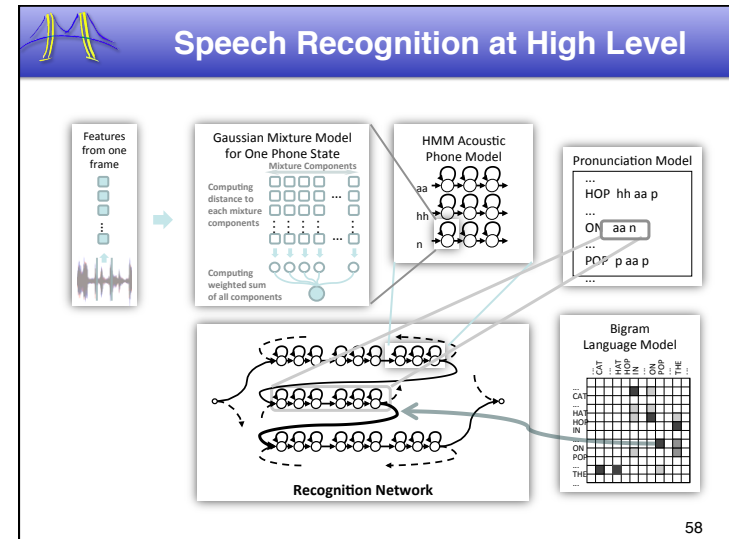
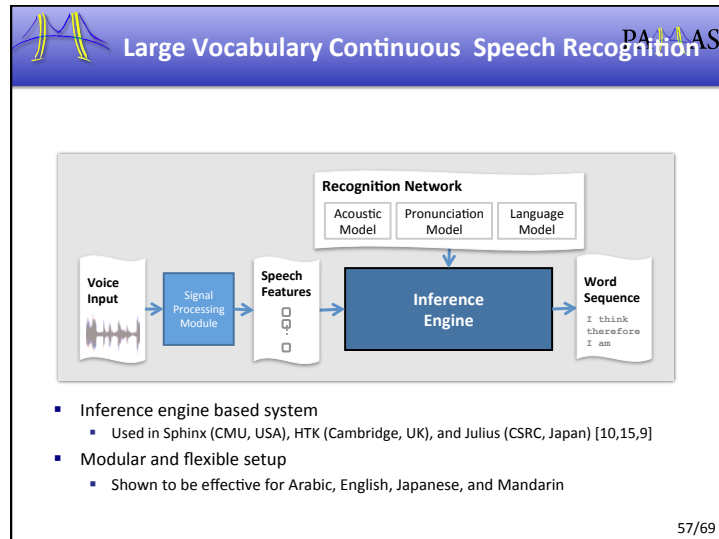
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55

Automatic Speech Recognition

56



HMMs for speech

Word Model

Observation Sequence (spectral feature vectors)

Dan Klein's CS288, Lecture 9 61

Iterative Refinement Structural Pattern

Hidden Markov Model

- One iteration per time step
- Identify the set of probable states in the network given acoustic signal given current active state set
- Prune unlikely states
- Repeat

Structural Patterns	Model-View-Controller
Pipe-and-Filter	Iterative-Refinement
Agent-and-Repository	Map-Reduce
Process-Control	Layered-Systems
Event-Based/Implicit-Invocation	Arbitrary-Static-Task-Graph
Puppeteer	

62/69

Inference Engine in LVCSR

- Three steps of inference
 0. Gather operands from irregular data structure to runtime buffer
 1. Perform observation probability computation
 2. Perform graph traversal computation

Parallelism in the inference engine:

0. Gather operand

1. $P(x_t|s_t)$

2. $m[t][s_t]$

Viterbi Algorithm

63/69

Each Filter is a Map Reduce

0. Gather operands

$$m[t][s_t] = \max_{s_{t-1}} m[t-1][s_{t-1}] \cdot P(s_t|s_{t-1}) \cdot P(x_t|s_t)$$

- Gather and coalesce each of the above operands for every s_t
- Facilitates opportunity for SIMD

Parallelism in the inference engine:

0. Gather operand

1. $P(x_t|s_t)$

2. $m[t][s_t]$

0. Gather operand

max

64/69

Each Filter is Map Reduce

1. observation probability computation

PAAS

$$m[t][s_t] = \max_{s_{t-1}} m[t-1][s_{t-1}] \cdot P(s_t | s_{t-1}) \cdot P(x_t | s_t)$$

- Gaussian Mixture Model Probability
- Probability that given this feature-frame (e.g. 10ms) we are in this state/phone

Parallelism in the inference engine:

0. Gather operand
1. $P(x_t | s_t)$
2. $m[t][s_t]$

1. $P(x_t | s_t)$

max

65/69

1. Observation Probability Computational Patterns

PAAS

- Observation probabilities are computed from Gaussian Mixture Models
 - Each Gaussian probability in each mixture is independent
 - Probability for one phone state is the sum of all Gaussians times the mixture probability for that state

$$p(x_j | \mu_i, \Sigma_i) = \sum_i \pi_i g(x_j | \mu_i, \Sigma_i)$$

$$= \sum_i \pi_i \frac{1}{(2\pi)^{\frac{d}{2}} |\Sigma_i|^{\frac{1}{2}}} \exp\{-\frac{1}{2}(x_j - \mu_i)^T \Sigma_i^{-1} (x_j - \mu_i)\}$$

Gaussian Mixture Model for One Phone State

Computational Patterns

Graph-Algorithms
Dynamic-Programming
Dense-Linear-Algebra
Sparse-Linear-Algebra
Unstructured-Grids
Structured-Grids

Graphical-Models

Finite-State-Machines
Backtrack-Branch-and-Bound
N-Body-Methods
Circuits
Spectral-Methods
Monte-Carlo

Dan Klein's CS288, Lecture 9

66/69

Each Filter is Map Reduce

2. graph traversal computation

PAAS

- **Map** probability computation across distributed data sets – perform multiplication as below
- **Reduce** the results to find the maximumly likely states

$$m[t][s_t] = \max_{s_{t-1}} m[t-1][s_{t-1}] \cdot P(s_t | s_{t-1}) \cdot P(x_t | s_t)$$

Parallelism in the inference engine:

0. Gather operand
1. $P(x_t | s_t)$
2. $m[t][s_t]$

max

2. $m[t][s_t]$

67/69

All together: Inference Engine in LVCSR

PAAS

- Put all together the inference engine is dynamic programming

Computational Patterns

Graph-Algorithms
Dynamic-Programming
Dense-Linear-Algebra
Sparse-Linear-Algebra
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Structured-Grids

Graphical-Models

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Parallelism in the inference engine:

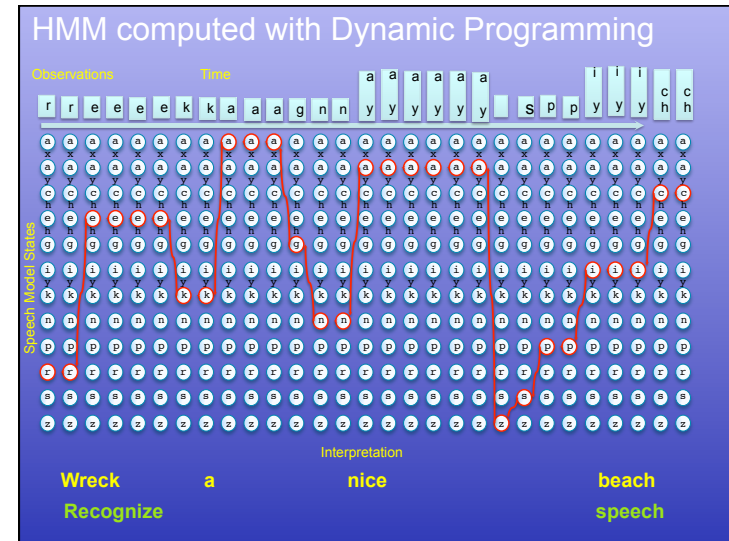
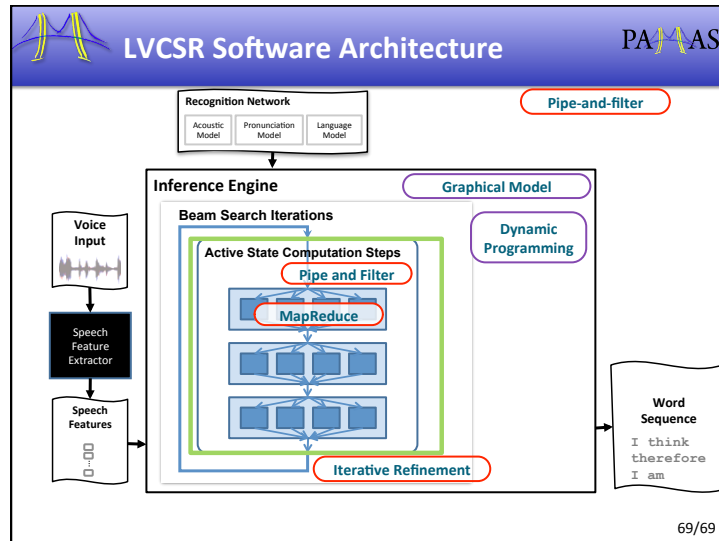
0. Gather operand

1. $P(x_t | s_t)$

2. $m[t][s_t]$

Viterbi Algorithm

68/69



This Approach Works

Application	Speedups	Reference
MRI	100x	IEEE TMI 2012
SVM-train	>3000 Downloads, 20x	ICML 2008
SVM-classify	109x	ICCV 2009
Contour	>3000 Downloads, 130x	ICCV 2009
Object Recognition	80x	WACV 2011
Poselet	20x	
Optical Flow	32x	ECCV 2010
Speech	11x	Interspeech 2010, 2011
Value-at-risk	60x	Wiley 2011
Option Pricing	25x	

"Considerations When Evaluating Microprocessor Platforms" In Proceedings of the 3rd USENIX conference on Hot topics in parallelism (HotPar '11). USENIX Association, Berkeley, CA, USA.

71/69

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 - High-level examples of architecting applications
 - ⇒ ■ Summary
- 72/69

Recap: Architecting Parallel Software PA4AAS

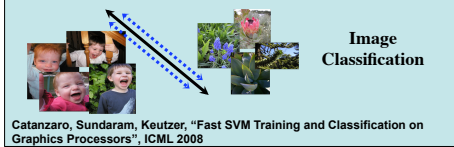
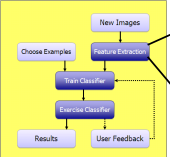
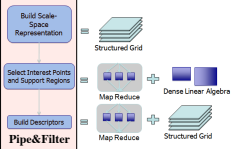
- Start with a compelling, performance sensitive application.
 

Image Classification
Catanzaro, Sundaram, Keutzer, "Fast SVM Training and Classification on Graphics Processors", ICML 2008
- Define the overall structure

Identify the Software Structure
- Define computations inside structural elements

Identify the Key Computations
- Compose Structural and computational patterns to yield software architecture
 


"Image Feature Extraction for Mobile Processors", Mark Murphy, Hong Wang, Kurt Keutzer IISWC '09

73/69

OPL/PLPP 2012

Applications

Structural Patterns

- Pipe-and-Filter
- Agent-and-Repository
- Garlan and Shaw Architectural Styles

Computational Patterns

- Model-View-Controller
- Iterative-Refinement
- Map-Reduce
- Berkeley View 13 dwarfs
- Graph-Algorithms
- Dynamic-Programming
- Dense-Linear-Algebra
- Finite-State-Machines
- Task Decomposition
- Data Decomposition
- Design Evaluation

Find Patterns

Parallel Algorithm Strategy Patterns

- Task-Parallelism
- Divide and Conquer

Implementation Strategy Patterns

- SPMD
- Kernel-Par.
- Program structure
- Fork/Join
- Actors
- Vector-Par

Parallel Execution Patterns

- Coordinating Processes
- Stream processing

Discrete-Event Geometric-Decomposition Speculation

Queue-Driven Map-Reduce

Distributed-Array Shared-Data

Rel Graph Traversal

Algorithms and Data structure

Shared Address Space Threads

Task Driven Execution

Concurrency Foundation constructs (not expressed as patterns)

Thread/proc management

Communication

Synchronization

Computational Patterns Make me Feel Smart

Apps	Embed	SPEC	DB	Games	ML	HPC	CAD	Health	Image	Speech	Music	Browser
Graph Algorithms												
Graphical Models												
Backtrack / B&B												
Finite State Mach.												
Circuits												
Dynamic Prog.												
Unstructured Grid												
Structured Grid												
Dense Matrix												
Sparse Matrix												
Spectral (FFT)												
Monte Carlo												
N-Body												

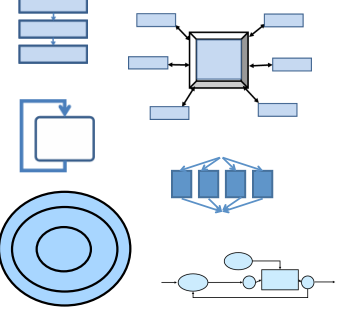
- For many years computation has been like a big ball of yarn
- Computational patterns help us to unravel it into 13 strands
- Alan Kay "Perspective is worth 100 IQ points."
- Computational patterns give us perspective on computation

75/69

Structural Patterns Make me Feel Organized

Structural Patterns

- Pipe-and-Filter
- Agent-and-Repository
- Event-based
- Layered Systems
- Model-view-controller
- Arbitrary Task Graphs
- Puppeteer
- Iterator/BSP
- MapReduce



- The modularity provided by structural patterns make me feel organized.
- Even the most complex application can be broken down into manageable modules

76/69

Summary PAAS

- The key to productive and efficient parallel programming is creating a good software architecture – a hierarchical composition of:
 - Structural patterns: enforce modularity and expose invariants
 - I showed you six – four more will be all you ever need
 - Computational patterns: identify key computations to be parallelized
 - Orchestration of computational and structural patterns creates architectures which greatly facilitates the development of parallel programs:

Patterns: <https://patterns.eecs.berkeley.edu/>

77/69

More examples

78

Architecting Speech Recognition

Large Vocabulary Continuous Speech Recognition Poster: Chong, Yi
Work also to appear at Emerging Applications for Manycore Architecture

79

CBIR Application Framework

Catanzaro, Sundaram, Keutzer, "Fast SVM Training and Classification on Graphics Processors", ICML 2008

80

Feature Extraction

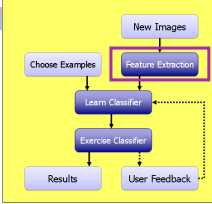
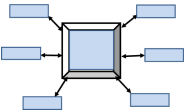


Image histograms are common to many feature extraction procedures, and are an important feature in their own right

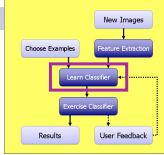
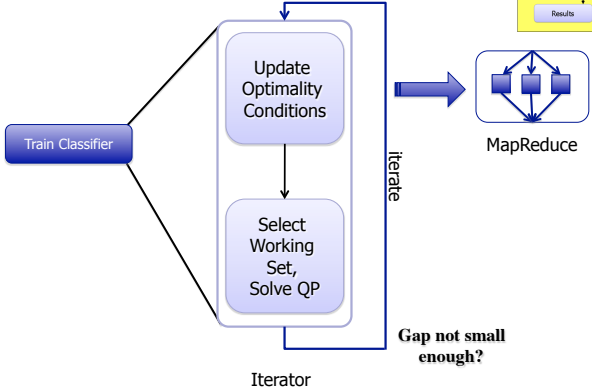


- Agent and Repository: Each agent computes a local transform of the image, plus a local histogram.
- Results are combined in the repository, which contains the global histogram

- The data dependent access patterns found when constructing histograms make them a natural fit for the agent and repository pattern

81

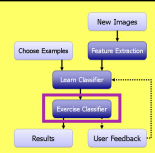
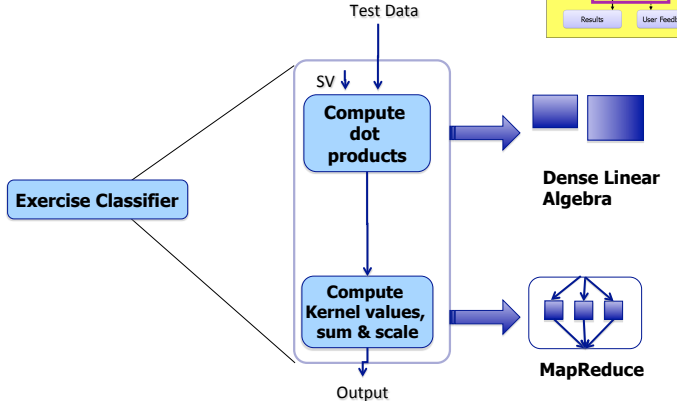
Train Classifier: SVM Training

Gap not small enough?

82

Exercise Classifier : SVM Classification

83

Reinvention of design?

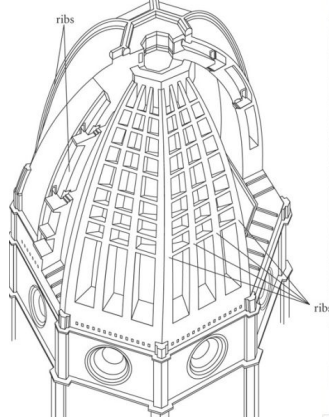
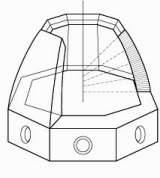


- In 1418 the **Santa Maria del Fiore** stood without a dome.
- Brunelleschi won the competition to finish the dome.
- Construction of the dome without the support of flying buttresses seemed unthinkable.

84

Innovation in architecture

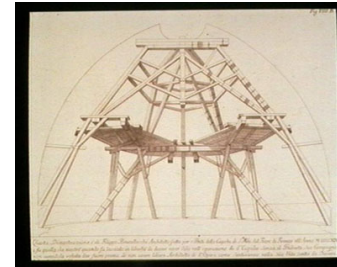
- After studying earlier Roman and Greek architecture, Brunelleschi drew on diverse architectural styles to arrive at a dome design that could stand independently



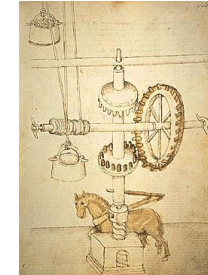
<http://www.templejc.edu/dept/Art/ASmith/ARTS1304/Joe1/ZoomSlide0010.html> 85

Innovation in tools

- His construction of the dome design required the development of new tools for construction, as well as an early (the first?) use of architectural drawings (now lost).



Scaffolding for cupola



Mechanism for raising materials

http://www.artist-biography.info/gallery/filippo_brunelleschi/67/

86

Innovation in use of building materials

- His construction of the dome design also required innovative use of building materials.



Herringbone pattern bricks

<http://www.buildingstonemagazine.com/winter-06/art/dome8.jpg>

87

Resulting Dome



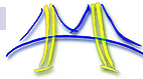
Completed dome



http://www.duomofirenze.it/storia/cupola_eng.htm

88

The point?



- Challenges to design and build the dome of **Santa Maria del Fiore** showed underlying weaknesses of architectural understanding, tools, and use of materials
- By analogy, parallelizing code should not have thrown us for such a loop. Our difficulties in facing the challenge of developing parallel software are a symptom of underlying weakness is in our abilities to:
 - Architect software
 - Develop robust tools and frameworks
 - Re-use implementation approaches
- Time for a serious rethink of all of software design