	P1			*P2*			*Bus*				*Memory*	
step	*State*	*Addr*	*Value*	*State*	*Addr*	*Value*	*Action*	*Proc.*	*Addr*	*Value*	*Addr*	*Value*
P1: Write 10 to A1	*Excl.*	*A1*	*10*				*WrMs*	P1	A1		A1	0
P1: Read A1	Excl.	A1	10								A1	0
P2: Read A1				*Shar.*	*A1*		*RdMs*	P2	A1		A1	0
	Shar.	A1	10				*WrBk*	P1	A1	10	A1	*10*
				Shar.	A1	10	*RdDa*	P2	A1	10	A1	10
P2: Write 20 to A1	*Inv.*			*Excl.*	A1	*20*	*WrMs*	P2	A1		A1	10
P2: Write 40 to A2							*WrMs*	P2	A2		A1	10
				Excl.	*A2*	*40*	*WrBk*	P2	A1	20	A1	*20*
10 to 14	15 to 23	Single Step										

Comments
Inv -> Ex; knock out any other copies
Read of Ex: Do nothing
Inv -> Sh; knock out any other copies to memory
Ex->Sh; write back data to memory
P2 reads data from memory
Sh->Ex; knock out any other copies
Ex->Ex: make sure no one has a copy of A2 (e.g., 2-way set assoc); don't read since 1 block wr
P2 writes back old copy